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Application / Conf. No.	10/659,978 / 5060					
Filing Date	September 11, 2003					
First Named Inventor	Eric D. Groen					
Art Unit	2817					
Examiner Name	Arnold M. Kinkead					
Attorney Docket Number	X-1433 US					

	U.S. PATENT DOCUMENTS						
Examiner Initials	Cite No.'	Document Number Number - Kind Code (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Floures Appear		
		US- 6,351,191 B1	02-26-02	Nair et al.			
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FOREIGN PATENT DOCUMENTS							
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)		Application / Conf. No.	10/659,978 / 5060	
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OTHER - NON PATENT LITERATURE DOCUMENTS					
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Del		John D. van der Tang et al.; "A 9.8-11.5 GHz Quadrature Ring Oscillator for Optical Receivers"; 2002 IEEE; IEEE Journal of Solid-State Circuits, Vol. 37, No. 3; March 2002; pp. 438-442.			
M		Seong-Jun Song et al.; "A 4-Gb/s CMOS Clock and Data Recovery Circuit Using 1/8-Rate Clock Technique"; 2003 IEEE; IEEE Journal of Solid-State Circuits, Vol. 38, No. 7; July 2003; pp. 1213-1219.			
	/	Werner Baumberger; "A Single Chip Image Rejecting Downconverter for the 2.44 GHz Band"; Gallium Arsenide Integrated Circuit (GAAS IC) Symposium; 1993; 15th Annual; 10-13 October 1993; pp. 37-40.			
\mathcal{L}		A Worapishet et al.; "An NMOS Inductive Loading Technique for Extended Operating Frequency CMOS Ring Oscillators"; The 2002 45th Midwest Symposium on Circuits and Systems; Conference Proceedings; August 4-7, 2002; IEEE, Vol. 1 of 3; 4 August 2002; pp. I-116 to I-119.			
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